

Appl. No. 10/708,662
Amdt. dated June 03, 2005
Reply to Office action of March 15, 2005

AMENDMENTS TO THE CLAIMS

1. (original) A method for a display controller to access data stored in a memory device of a computer device comprising:
 - 5 setting a block capacity value;
 - dividing a plurality of read requests corresponding to a predetermined request sequence and said block capacity value into a plurality of request, wherein a total amount of data required by read requests grouped in each request group is less than the block capacity value; and
 - 10 reordering the read requests in each of said request groups corresponding to data on the page of said memory device into a second request sequence for each of said request groups;
 - executing the read requests in each of request group according to said second request sequence of each of said request groups.
- 15 2. (original) The method of claim 1 further comprising: when the pages accessed by the read requests in the next request group is as same as the page accessed by the final read request in the last request group,
 - 20 executing said read requests in the next request group at first, and then executing other read requests in the next request group.
3. (currently amended) The method of claim 1 wherein the computer device comprises a memory controller that stores the plurality of read requests in a queue.
- 25 4. (currently amended) The method of claim 1 wherein the computer device comprises a memory controller ~~[[is]]~~ installed in a north bridge circuit and the north bridge circuit is used for controlling transmission between a display controller and the memory device.

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5. (currently amended) The method of claim 1 wherein the computer device comprises a memory controller, and ~~[[the]]~~ data that are read with the memory controller are transmitted to a display controller.
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6. (original) The method of claim 5 wherein the display controller is connected electrically to the memory controller through an accelerated graphics port bus in the computer device.
- 10 7. (original) The method of claim 5 wherein the display controller is a graphics card.
8. (original) The method of claim 5 wherein the display controller is installed in a north bridge circuit in the computer system.
- 15 9. (original) The method of claim 1 wherein the memory device is a system memory of the computer system.
10. (currently amended) The method of claim 1 wherein the computer device comprises a memory controller that stores the data in the display controller according to the predetermined request sequence.
- 20
11. (original) A method for accessing data, a plurality of read requests used for accessing data from a memory device according to a predetermined request sequence, the method comprising:
- 25 reordering said read requests according to pages in said memory device accessed by said read requests in a second request sequence, wherein said read requests accessed the same page of said memory device are continuously arranged; and executing the read requests according to said second request sequence.

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12. (currently amended) The method of claim 11 further comprising:
when the pages in the memory device of the read requests in the next request group
include pages that are the same as the page corresponding to the final read
5 request in the last request group,
executing the read requests in the next request group corresponding to the data in the
page ~~with the memory controller~~ and then executing the read requests in the
next command block corresponding to the data of the different page.
- 10 13. (currently amended) The method of claim 11 wherein ~~[[the]]~~ a memory controller is
used to store ~~stores~~ the plurality of read requests in a queue.
14. (currently amended) The method of claim 11 wherein ~~[[the]]~~ a memory controller is
installed in a north bridge circuit and the north bridge circuit is used for controlling
15 the transmission between a display controller and the memory device.
15. (currently amended) The method of claim 11 wherein ~~[[the]]~~ data ~~[[that]]~~ are read
with ~~[[the]]~~ a memory controller and are transmitted to a display controller.
- 20 16. (original) The method of claim 15 wherein the display controller is connected
electrically to the memory controller through an accelerated graphics port bus in the
computer device.
17. (original) The method of claim 15 wherein the display controller is a graphics card.
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18. (original) The method of claim 15 wherein the display controller is installed in a
north bridge circuit in the computer device.

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19. (original) The method of claim 11 wherein the memory device is a system memory of the computer device.
20. (currently amended) The method of claim 11 wherein [[the]] a memory controller is
5 used to store ~~stores the~~ data in [[the]] a display controller according to the predetermined request sequence.